

Massachusetts Institute of Technology
Department of Electrical Engineering and Computer Science
6.111 - Introductory Digital Systems Laboratory

Final Project Check Off Sheet

Project Title: The J Computer
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TA Signature/Date:

Design & Implementation

System and Module Block Diagrams
Source code and FSM State Transition Diagrams

Functionality

RS-232: demonstrate buffered RS-232 UART in HyperTerminal
Hello World!: execute simple method from ROM, display text to RS-232
Code installation: download and run compliant Java class files using desktop client
Hardware integration: demonstrate Java/hardware interaction via sample programs that manipulate alpha blending overlay manager, and retrieve PS/2 device data
Final demo: use keyboard/mouse to play Java MIT Pong, as described in Lab4