

Final Project Check Off Sheet

Project Title: Digital Audio Processor

Student Names: William Buttinger, Dimitri Podoliev

TA Name: Amir Hirsch

TA Signature/Date:

William Buttinger:

Router Module

RS232 interface module

Dimitri Podoliev

Audio I/O control module

Low Pass Filter module

High Pass Filter module

Band Pass Filter module

Delay module

User inputs interface module

Group

Top level interconnection module

Additional modules (if time permits)

Compressor module

Expander module

Mixer module

Computer based graphical user interface