

6.111 Final Project Check Off Sheet

Project: Snapshot! (FPGA based digital camera)

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TA: Javier Castro

TA Signature:

Sanjay:

- Continuous interlaced write & read of color video signal to/from single ZBT SRAM.
- Incorporate use of computer mouse to control camera functionality through user interface on computer screen.
- Stop write and read of video signal when picture capture request is asserted and image is being compressed and stored.
- Generate user interface using computer monitor to display live video as well as stored images.

Michael:

- Upon receiving request for picture capture, uncompressed image should be read from one SRAM, compressed using a 2D-DCT, and stored in the labkit's other SRAM. If this SRAM is full, a deny signal should be outputted to user and the image will not be saved.
- Upon receiving request for picture review, compressed image should be read from the storage SRAM, uncompressed using a 2D-IDCT, and written to labkit's other SRAM.
- Create memory manager to record size and location of each image stored in memory.
- Upon receiving delete picture request, the memory manager should update its memory map to allow selected image to be written over.
- Allow user to change input image size and compression level through labkit's switches.