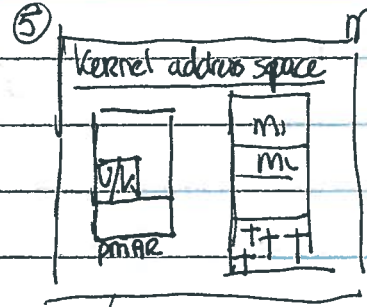
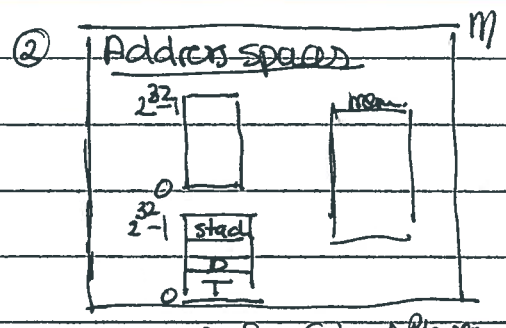
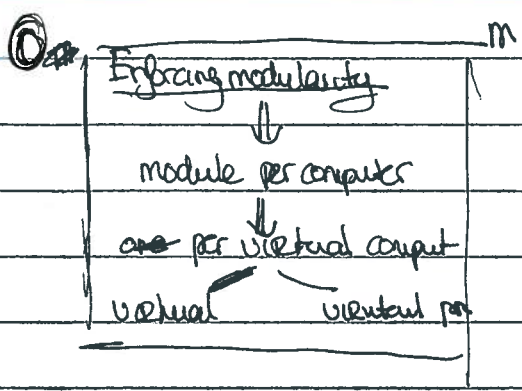


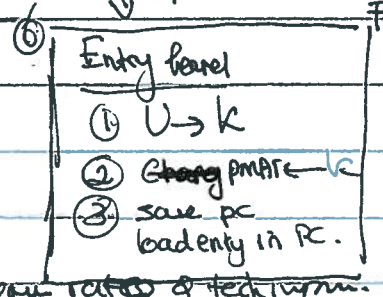
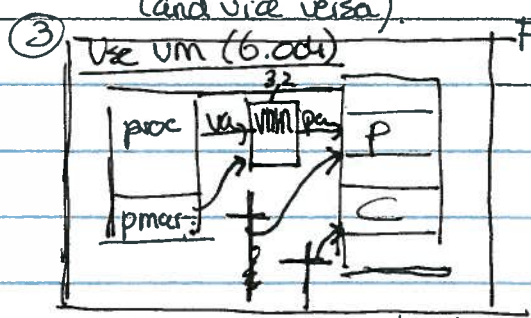
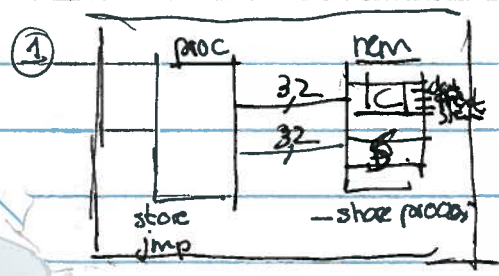
- x86 took more time
- need 5 minutes just first part
- or make x86 a running example

L4 - fault isolation with VM (2/15/2004)



impossible for C to eg P's mem. (and vice versa)

supervisor call

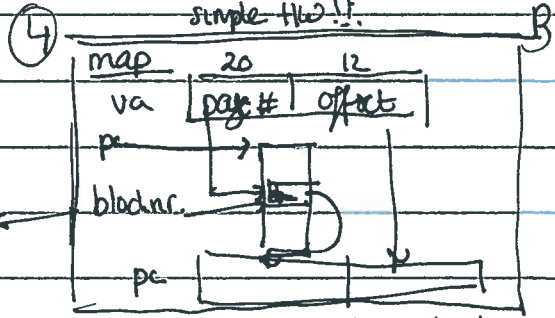
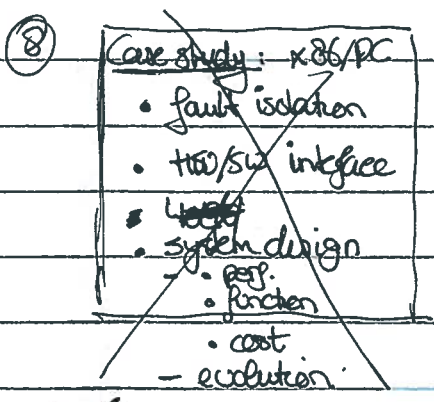


(debugging difficult)

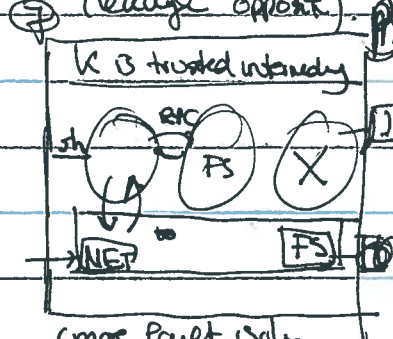
(old OSes, there are 25 in this way)

each module its own trap
 - register defines which process
 - p+c can only ref what in their map!
 simple tho!!

memory tables of tech system
 (drive prelude, include codes, x86 → P slow
 (also for interrupt + illegal instr.)
 (change operant))



- one entry per address too by
 - one entry per AS inflexible



(more fault isol., by putting modules in their own AS but req cost)

who manages page maps?

wh manage pmAR.

calculator
 7/10/04 22:30
 2000 - 2001
 2000 - 2001
 2000 - 2001
 2000 - 2001