

Massachusetts Institute of Technology
Department of Electrical Engineering and Computer Science
6.012
Microelectronic Devices and Circuits
Spring 2007
February 14, 2007 - Homework #1
Due - February 21, 2007

Problem 1

A piece of silicon is doped with $N_a = 2 \times 10^{15} \text{ cm}^{-3}$ and $N_d = 1 \times 10^{15} \text{ cm}^{-3}$

- a) What is the majority carrier? Is the silicon type n or type p?
- b) Find the electron and hole concentration and mobility at room temperature.
- c) We want increase the electron concentration to $1 \times 10^{17} \text{ cm}^{-3}$. What is the additional dopant type and concentration? What is the new electron mobility?

a) $N_a > N_d$. **Holes. P-type.**

b) Total impurities = $N_a + N_d = 3 \times 10^{15} \text{ cm}^{-3}$.

According to figure 2.8.

$$\mu_p = 475 \text{ cm}^2 (\text{Vs})^{-1}$$

$$\mu_n = 1300 \text{ cm}^2 (\text{Vs})^{-1}$$

$$N_a - N_d \gg n_i$$

$$p_0 = N_a - N_d = 10^{15} \text{ cm}^{-3}$$

$$n_0 = n_i^2 / p_0 = 10^5 \text{ cm}^{-3}$$

$$c) N_d' - N_a = 10^{17}$$

$$N_d' = N_d(\text{current}) + N_d(\text{additional})$$

Need **1.01×10^{17}** additional donors per cm³.

$$\mu_n = 775 \text{ cm}^2 (\text{Vs})^{-1}$$

Problem 2

A piece of silicon is doped with $N_d = 1 \times 10^{15} \text{ cm}^{-3}$. Below is a table for the intrinsic electron concentration for three different temperatures.

n_i	Temperature
$1 \times 10^{10} \text{ cm}^{-3}$	300 K (room temp.)
$1 \times 10^{15} \text{ cm}^{-3}$	600 K
$1 \times 10^{17} \text{ cm}^{-3}$	1150 K

- a) Calculate the total hole and electron concentration for all three different temperatures.

a)

$$T = 300, \quad N_d \gg n_i$$

$$n = N_d = 1 \times 10^{15} \text{ cm}^{-3}$$

$$p = n_i^2 / n = 1 \times 10^5 \text{ cm}^{-3}$$

$$\begin{aligned}
 T = 600, \quad N_d \sim n_i \\
 n = N_d/2 + N_d/2(1 + 4n_i^2/N_d^2)^{1/2} = 1.62 \times 10^{15} \text{ cm}^{-3} \quad p = n_i^2/n = 6.18 \times 10^{14} \text{ cm}^{-3} \\
 T = 1150, \quad N_d \ll n_i \\
 n = n_i = 1 \times 10^{17} \text{ cm}^{-3} \quad p = n_i^2/n = 1 \times 10^{17} \text{ cm}^{-3}
 \end{aligned}$$

Problem 3

Given a uniformly n-type ion-implanted layer with thickness $t = 1 \text{ } \mu\text{m}$ and doping concentration $N_d = 10^{17} \text{ cm}^{-3}$.

- What is the sheet resistance?
- What is the resistance of the layout shown below? Assume that the contacts each contribute .65 squares.



- By adding additional dopants, we make a new n-type ion-implanted resistor with an average doping concentration $N_{d1} = 2 \times 10^{17} \text{ cm}^{-3}$ over the depth $0 < d < 0.5 \text{ } \mu\text{m}$ and $N_{d2} = 10^{17} \text{ cm}^{-3}$ over the depth $0.5 \text{ } \mu\text{m} < d < 1 \text{ } \mu\text{m}$. Find the new sheet resistance.

$$a) (qN_d\mu_{nt})^{-1} = (1.6 \times 10^{-19} \times 10^{17} \times 775 \times 10^{-4}) = \mathbf{806 \text{ } \Omega/\phi}$$

Note $t = 10^{-4} \text{ cm}$.

$$b) \text{ Total number of squares} = .65 \times 2 + 80/4 = 21.3$$

$$\text{Resistance} = \Omega_{\phi} \times \text{number of squares} = 806 \times 21.3 = \mathbf{17.2 \text{ k } \Omega}$$

c) Think of this as two resistors in parallel; one on top and one on bottom.

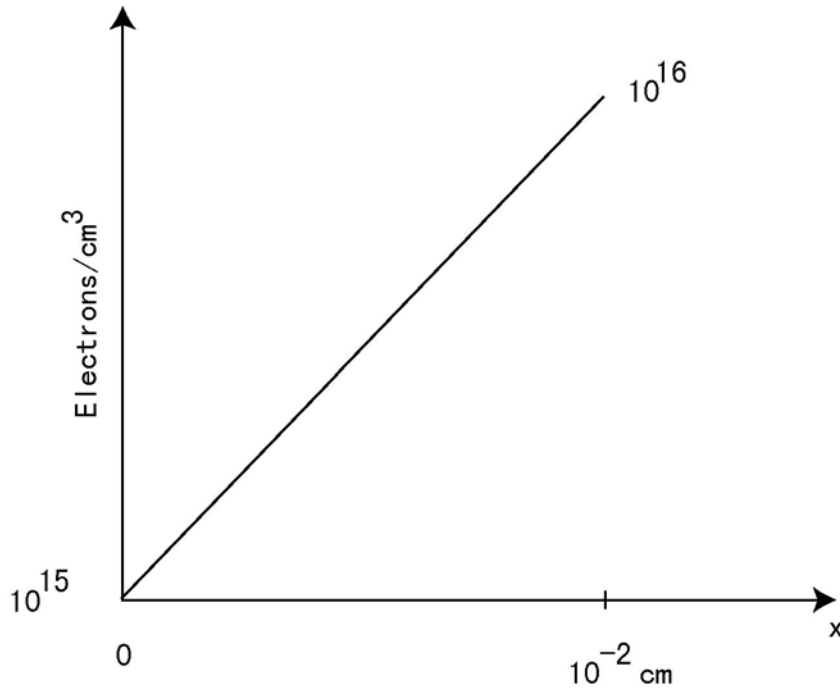
$$\Omega_{\text{top}/\phi} = (1.6 \times 10^{-19} \times 2 \times 10^{17} \times 600 \times 5 \times 10^{-5})^{-1} = 1041 \text{ } \Omega/\phi$$

$$\Omega_{\text{bot}/\phi} = (1.6 \times 10^{-19} \times 10^{17} \times 775 \times 5 \times 10^{-5})^{-1} = 1613 \text{ } \Omega/\phi$$

$$\text{Total sheet resistance} = (\Omega_{\text{top}/\phi}) // (\Omega_{\text{bot}/\phi}) = 1041 \times 1613 / (1041 + 1613) = \mathbf{633 \text{ } \Omega/\phi}$$

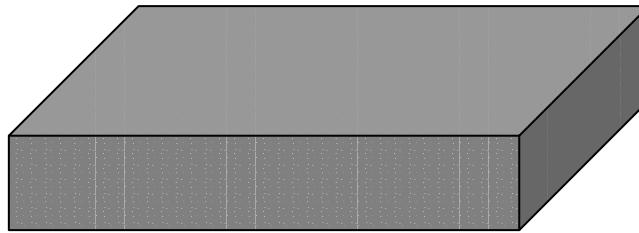
Problem 4

A slab of silicon has the following electron distribution.



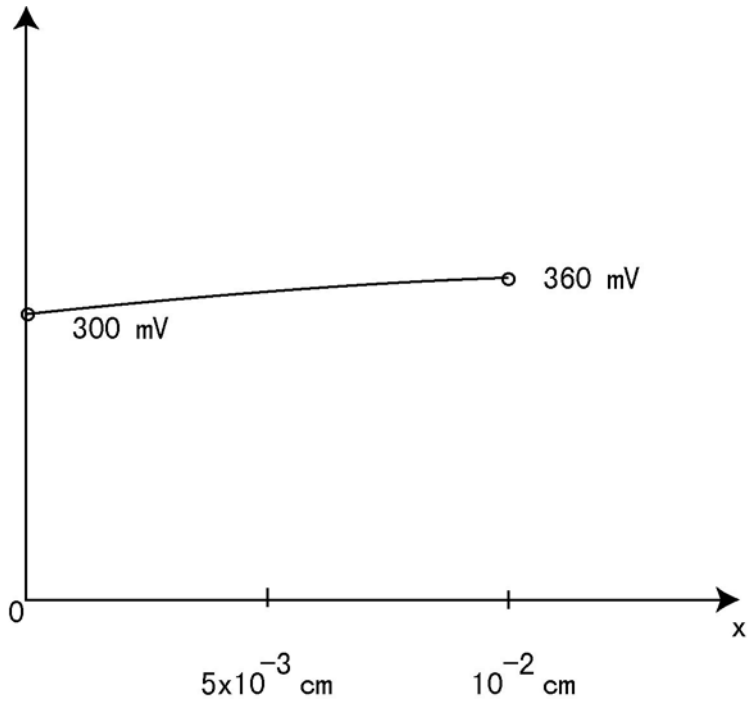
scales are linear

- Assume thermal equilibrium. Plot the potential ϕ .
- What is the electron diffusion current density? Hole diffusion current density?
Assume $D_n = 2 \times D_p = 26 \text{ cm}^2/\text{s}$
- The hole and electron diffusion current densities do not sum to zero; however, the silicon cannot have a net current since it is an open circuit. Explain what is happening.



Silicon Slab

- Using $\phi(x) = 60 \text{ mV} * \log(n(x)/10^{10})$



b) The slope of the line is $10^{16} - 10^{15} / 10^{-2} = 9 \times 10^{17}$.
 $J_{diff,n} = qD_n(dn/dx) = 26 \times 9 \times 10^{17} \times 1.6 \times 10^{-19} = 3.744 \text{ A cm}^{-2}$

Assume room temperature, so $p = n_i^2/n$. To simplify the problem, assume that the hole concentration is also in a straight line. Using the endpoints, $p(x=0) = 10^5$ and $p(x=10^{-12}) = 10^4$. Slope is -9×10^6 .

$$J_{diff,p} = -qD_p(dp/dx) = 0.5 \times 26 \times 9 \times 10^6 \times 1.6 \times 10^{-19} = 1.872 \times 10^{-11} \text{ A cm}^{-2}$$

Notice $J_{diff,n} \gg J_{diff,p}$

c) The voltage difference across the silicon produces a drift current that cancels out the diffusion current. Again, the electron drift current is much larger than the hole drift current because the electron concentration is much greater.